

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

21907 U.S. PTO  
104621852  
07/16/03

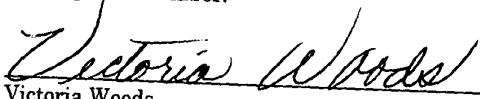
In re Application of: ) DIVISIONAL  
Inventors: Yanjun Ma, David Russell ) PATENT APPLICATION  
Evans, Yoshi Ono, and ) July 16, 2003  
Sheng Teng Hsu )  
Serial No.: Unknown ) Attorney Docket No.  
Filed: Herewith ) SLA0815 (SMT324D)  
Title: SUB-MICRON MOS ) Parent Application:  
TRANSISTOR ) Serial No. 09/783,760  
 ) Filed: February 14, 2001  
 ) Pending in Group Art  
 ) Unit 2825  
 ) Examiner: Victor V.  
 ) Yevsikov  
 ) Anticipated Classification:  
 ) Class: 257  
 ) Subclass: 288

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**CERTIFICATE OF MAILING BY "EXPRESS MAIL"  
UNDER 37 C.F.R. 1.10**

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Victoria Woods  
Signature Date: July 16, 2003

DIVISIONAL APPLICATION UNDER 37 C.F.R. §1.53(b)

17668 U.S. PTO  
07/16/03

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Hon. Commissioner for Patents  
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Sir:

The undersigned, attorney-of-record in parent application Serial No. 09/783,760, filed February 14, 2001, entitled "Structure and Method of Making a Sub-Micron MOS Transistor," hereby requests the filing of a Divisional Application under the provisions of 37 C.F.R. §1.53(b).

Identification of the Parent of the Present Divisional Application:

Serial No.: 09/783,760  
Filed: February 14, 2001  
Title: Structure and Method of Making a Sub-Micron MOS Transistor  
Inventors: Yanjun Ma, David Russell Evans, Yoshi Ono, and Sheng Teng Hsu  
Status: Pending

Copy of Present Divisional Application:

Enclosed is a copy of the present divisional application which incorporates the following changes from the as-filed specification and drawings in parent application Serial No. 09/783,760: The titled has been revised, the introductory cross-reference section has been changed, formal drawings have replaced the as-filed informal drawings, and a typographical error was corrected on page 3. The present divisional application includes the following papers:

- No. of Pages in Specification: 11 No. of Claims: 17
- No. of Sheets of Drawings: 2 Formal: X or Informal: \_\_\_\_\_
- Copy of the Declaration (signed by all inventors) in parent application Serial No. 09/783,760.

X Copy of the Power of Attorney in parent application Serial No. 09/783,760.

Parent application Serial No. 09/783,760, filed February 14, 2001, entitled "Structure and Method of Making a Sub-Micron MOS Transistor" is hereby incorporated herein by reference.

Delete the Following Inventor(s) for This Application: None

Amendments:

X Please amend the enclosed specification as follows:

Cancel claims 1-15.

Filing Fee

The Divisional Patent Application filing fee is calculated as shown below after cancellation of claims 1 through 39:

No. Filed	No. Extra	Rate Small Entity/ Other Than Small Entity
Basic		\$375.00
Fee		\$750.00
		\$ 750.00
Total		\$ 9.00
Claims <u>2</u>	(20)	= <u>0</u> X \$18.00 = \$ 00.00
Independent		\$42.00
Claims <u>1</u>	(3)	= <u>0</u> X \$84.00 = \$ 00.00
First Presentation of		\$140.00
Multiple Dependent Claim(s) <u>0</u>		\$280.00 = \$ 00.00
Total PATENT APPLICATION FILING FEE		\$ 750.00

\* If the difference is less than zero, enter "0".

\*\* Count claims after any amendment.

The total fee associated with this communication is as follows:

Patent application filing fee	\$ 750.00
	=====
<b>TOTAL FEE DUE:</b>	<b>\$ 750.00</b>

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- Please charge Deposit Account No. 19-1457 in the amount of \$ 750.00.  
A duplicate copy of this authorization is enclosed.
- The Commissioner is hereby authorized to charge underpayment  
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**Correspondence Address**

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Respectfully submitted,

By:

David C. Ripma  
Reg. No. 27,672

Date: 7/16/03

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